

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: Takano et al.

FILED: August 27, 2003

FOR: Semiconductor Integrated Circuit Having Logic Circuit Comprising
Transistors With Lower Threshold Voltage Values and Improved
Pattern Layout

DOCKET: NEC OSP-9770 DIV II

MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to Examination of the subject Divisional Application, please amend the application
as follows:

Amendments to the Specification begin on page 2 of this Amendment.

Amendments to the Claims begin on page 5 of this Amendment.

Remarks/Arguments begin on page 7 of this Amendment.

HAYES SOLOWAY P.C.

130 W. CUSHING ST.
TUCSON, AZ 85701
TEL. 520.882.7623
FAX. 520.882.7643

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175 CANAL STREET
MANCHESTER, NH 03101
TEL. 603.668.1400
FAX. 603.668.8567